



AMENDMENT AFTER FINAL
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

SHINOZAKI

Serial Number: 09/385,014

Group Art Unit: 2816

Filed: August 27, 1999

Examiner: D. LE

For: INPUT CIRCUIT HAVING CURRENT REGULATING TRANSISTOR

RESPONSE TO FINAL REJECTION UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

Date: May 14, 2001

Sir:

In response to the Office Action dated February 13, 2001, please amend the above-identified application as set forth below.

IN THE CLAIMS:

Please amend claim 1 as follows. Pursuant to 37 C.F.R. § 1.121, as amended, a copy of the marked-up original claim 1 is attached to this Response showing the changes made therein.

--1. (Twice Amended) An input circuit comprising:

a differential circuit including a first transistor for receiving an external signal and a second transistor for receiving a reference signal, wherein sources of the first and second transistors are connected in common, and the differential circuit generates an

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